CS61C: Processor Design

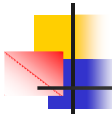## CS61C Fall2007 - Discussion #10
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Design A Processor

- Terms
  - SuperScalar – Execute n>2 instructions at once
  - OOO – Execute instructions out of order
  - VLIW – Multiple instructions executed as one
  - MultiCore – Execute multiple threads at once
- Design Ideas
  - Simplified instruction sets (SBN)
  - Eliminate the stack (memory is slow)
  - Eliminate branches (hard to optimize)
  - Some for of multicore?